

<b>Notice of References Cited</b>	Application/Control No. 10/501,903	Applicant(s)/Patent Under Reexamination VORBACH ET AL.	
	Examiner JUE WANG	Art Unit 2193	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hartenstein et al. "Parallelizing Compilation for a Novel Data-Parallel Architecture", 1995, PCAT-94, Parallel Computing: Technology and Practice.
	V	Hartenstein et al. "A Two-level Co-Design Framework for Xputer-based Data-driven Reconfigurable Accelerators", 1997, Proceedings of the Thirtieth Annual Hawaii International Conference on System Sciences.
	W	Jurgen Becker, "A Partitioning Compiler for Computers with Xputer-based Accelerators", 1997, Kaiserslautern University.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.